

**ENGINEERING CHANGE NOTE:**

ECN #:

388

ASSEMBLY: BMW4011E**DESCRIPTION:** PXY CPU CARD**New Assembly:****DATE:** 26/09/00**Current Assy Rev:** N**Current Schematic Rev:** 5.8**New Assy Rev:** P**New Schematic Rev:** 5.9

☐ CMS BOMs Updated ☐ Excel BOMs Updated ☒ Subcontractor Records Updated **Compatibility Maintained:** Yes

IMPLEMENTATION INFORMATION:

- ☐ Safety Issue ☐ UL Compliance Issue ☐ New Feature ☐ Cost Reduction
☒ Bug Fix ☐ EMC Compliance Issue ☐ Quality Issue
☐ Reliability Reasons ☐ Software Change ☐ Cosmetic Change

REASON FOR CHANGE:

To stop Bus Errors when running jtag test on startup after U21 is removed (as per Rev 5.7-5.8 change for new PCBs)

This ECN is mandatory for all Rev 5.8 cards. Note that step 2 must be done even if step 1 requires no action due to U21 still on the PCB.

DETAILS OF CHANGE:

1. If U21 is removed
connect U21/1 pad to U26/13 (74HC14)
connect U21/21 pad to U26/12 (74HC14)
If U21 is on the PCB, no action is required for this step and U21 can be left on the board.

2. Place revision resistors (RMD8573 - RES 10K 0.125W 1% SMD0603) as follows;

R127 - Omit

R128 - 10k

R133 - 10k

R134 - Omit

R135 - 10k

R136 - Omit

R137 - Omit

R138 - 10k

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Place a assembly revision label on the card, marked "P", after the blank Rev 5 PCB P/N so it reads as " BMW4011E-P".

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HW or Project Engineer.....

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R&D or Project Manager.....